

## REGISTRATION FORM

A Three Day Faculty Workshop on  
"Recent Advances in Green Energy  
Technologies in Allied Power, Energy &  
Control Applications"  
28 - 30 May, 2018

1. Participant's Name :
2. Qualifications & Experience :
4. Department :
5. Organization :
6. Address for Correspondence :
7. Phone (O) :  
Mobile :
8. E-Mail :
9. Registration Fee : Rs. \_\_\_\_\_  
D.D No., Date and Bank Details :
10. Accommodation : Yes / No : \_\_\_\_\_

### DECLARATION

The information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop.

Place: \_\_\_\_\_  
Date: \_\_\_\_\_ Signature of the Applicant

### SPONSORSHIP CERTIFICATE

This is to certify that Mr./Ms. ....  
..... is an employee of our institution since ..... and is hereby sponsored by attend the Faculty Workshop on **A Three Day Faculty Workshop on "Recent Advances in Green Energy Technologies in Allied Power, Energy & Control Applications"** at VFSTR, Vadlamudi on 28 - 30 May, 2018.

Place: \_\_\_\_\_  
Date: \_\_\_\_\_ Signature of the Sponsoring Authority. (with seal)

## ORGANISING COMMITTEE

### Patron

**Dr. M.Y.S. Prasad**  
Vice-Chancellor, VFSTR, Vadlamudi

### Convenor

**Dr. G. Srinivasa Rao**  
Dean R&D,  
HOD, Department of EEE,  
VFSTR, Vadlamudi

### Coordinators

**Mr. A. Srihari Babu**  
Asst. Professor, Department of EEE, VFSTR  
**Mr. A.R. Vijay Babu**  
Asst. Professor, Department of EEE, VFSTR

### Members

**Mr. P.V.S. Sobhan**, Assoc. Prof.  
**Dr. K. Mercy Rosalina**, Assoc. Prof.  
**Mr. M. Subba Rao**, Asst. Prof.  
**Dr. K. Suresh**, Asst. Prof.  
**Mr. Ch. Umamaheswara Rao**, Asst. Prof.  
**Mr. N. Narasimha Rao Ch.**, Asst. Prof.  
**Mr. Bala Krishna K.**, Asst. Prof.  
**Mr. A. Naveen Reddy**, Asst. Prof.  
**Ms. K. Rachananjali**, Asst. Prof.

### All correspondence may be addressed to:

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# A Three Day Faculty Workshop on "Recent Advances in Green Energy Technologies in Allied Power, Energy & Control Applications"

28 - 30 May, 2018



Organized by

Department of

Electrical & Electronics Engineering,  
**VIGNAN'S FOUNDATION FOR SCIENCE,  
TECHNOLOGY & RESEARCH**

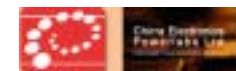


**VIGNAN'S**  
Foundation for Science, Technology & Research  
(Deemed to be University)  
- Established by UGC Act 1956

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## ABOUT VFSTR

Vignan's Foundation for Science, Technology & Research is the flagship institution of Vignan Group of Educational Institutions, is a NAAC 'A' accredited institution. Located in serene environs of Vadlamudi on the Guntur-Tenali highway, VFSTR with its sprawling play grounds, campus greenery and imposing academic blocks, is a virtual haven of rural quiet and idyllic beauty. Since its inception in 1997, the institution has been striving to promote high standards in technical education to aid in the career building of many students who step into its portals. Through diverse programs and updated curriculum by imparting industry exposure and hands-on skills, VFSTR trains its students into competitive and global professionals, imbued with ethical consciousness and social awareness. All the departments are supported by a good mix of young and senior faculty with a rich research, teaching and industry background. The sophisticated laboratories and research centres make it one of the most preferred institutions for the aspirants of engineering studies.

## ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was established in the year 1999 with the motto: *Excellence in Engineering Education*. The department strives for excellence in teaching, research and consultancy services to the industry. It has eminence in selected niche areas of Electrical and Electronics Engineering. The Department has highly qualified and experienced faculty with a strong research and professional expertise. Besides teaching undergraduate and post-graduate courses, the faculty members are engaged in a broad range of research and development areas in collaboration with renowned overseas universities, research institutions and multinational companies. The Department consists of 29 Research Scholars. The aim is to produce engineers who are flexible across disciplines and able to apply their knowledge and skills to solve complex problems. The department aims to produce thinking engineers, who will create new technologies for the future. The Department of Electrical and Electronics Engineering currently offers B.Tech in EEE, M.Tech programme in Power Electronics & Drives and Power Systems besides the Ph.D programme.

## OBJECTIVE OF THE WORKSHOP

Now-a-days a control strategy based on intuition and judgments has been emerged and is being fueled by recent advances in computing technologies. It has been given a collective name "Intelligent control" because of its performance, adaptability and simplicity.

The goal of this workshop is to enhance the knowledge and skill of faculty, students and industry professionals in the Rapid Control Prototyping (RCP) solutions to quickly develop, iterate and test control strategies in powerful hardware and comprehensive software tools for real-time applications. This workshop is also intended to give a complete hands on training on Design on Power Electronics Convertors (PEC) and Solar Energy Systems where control logic will be developed using schematic based design tool with dynamic software to monitor and analyze current and voltage waveforms with the ability to change control variables online.

## WORKSHOP COVERS

- Architectures for intelligent control
- Modeling intelligent systems
- Design techniques for intelligent controllers
- Advances in control system design for Aerospace Vehicles
- Introduction to dSpace based control solutions using schematic based Design
- Simple PWM generation and implementation in hardware
- Three phase sinusoidal PWM generation and implementation in hardware
- Open loop control of PMSM Motor using dSpace and implementation in hardware
- Closed loop current control of PMSM motor using dSpace and implementation hardware
- Solar MPPT Controller and hardware
- Grid integration of Renewable Energy Technologies & Role of Energy Storage.

## RESOURCE PERSONS

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**Mr. R. Laxman**, Scientist-'D', Research Centre Imarat (RCI), DRDO, Hyderabad

**Mr. Abhishek Mangalick**, CEO, Eco-sense Sustainable Energy Pvt. Ltd., New Delhi

**Dr. M. Amarendra**, HOD-EEE, KITS, Guntur

**Mr. V. Nagesh**, CTO, Chirra Power Converters Pvt. Ltd., Bangalore

**Mr. M. Rayudu**, CEO, Chirra Power Converters Pvt. Ltd., Bangalore

**Mr. H. Pavan**, Sr. Engineer, Dyna Fusion Pvt. Ltd., Bangalore

### ELIGIBILITY:

Faculty members of Engineering / Polytechnic Colleges / Researchers / Practicing Engineers /PG students and Ph.D scholars are eligible.

### REGISTRATION :

Scientists & Industrial participants	: Rs. 2,000/-
Faculty from Institutions	: Rs. 1,500/-
PG students & Ph.D.Scholars	: Rs. 1,000/-

Participants can register by sending the registration form along with Demand Draft in favour of "**VFSTR, General Funds**" payable at Guntur and it should reach on or before **24<sup>th</sup> May, 2018**.

To  
**Dr. G. Srinivasa Rao**  
Dean R&D,  
HOD, Department of EEE  
VFSTR, Vadlamudi – 522 213  
Guntur (Dist), Andhra Pradesh

### \*Spot Registrations are also available

For on line registration : <http://14.139.85.169/workshop>

- The registration fee includes Registration Kit, working lunch, tea and snacks during workshop.
- Accommodation will be provided on payment basis subject to availability.