

REGISTRATION FORM

A Five Day Faculty Workshop on
"Power Electronics Applications to Emerging
Trends in Energy Systems"
June 27th - July 1st, 2017

1. Participant's Name :
2. Qualifications & Experience :
4. Department :
5. Organization :
6. Address for
Correspondence :
7. Phone (O) :
Mobile :
8. E-Mail :
9. Registration Fee : Rs. _____
D.D No., Date and :
Bank Details :
10. Accommodation : Yes / No : _____

DECLARATION

The information furnished above is true to the best of my knowledge.
I agree to abide by the rules and regulations governing the workshop.

Place: _____
Date: _____ Signature of the Applicant

SPONSORSHIP CERTIFICATE

This is to certify that Mr./Ms.....
..... is an employee of our
institution since and is hereby sponsored by attend
the Faculty Workshop on A Five Day Faculty Workshop on
"Power Electronics Applications to Emerging Trends in Energy
Systems" at VFSTRU, Vadlamudi on June 27th - July 1st, 2017.

Place: _____
Date: _____ Signature of the Sponsoring
Authority.(with seal)

ORGANISING COMMITTEE

Patron

Dr. B. Ramamoorthy
Vice-Chancellor I/C, VFSTRU, Vadlamudi

Convener

Dr. G. Srinivasa Rao
Professor & Head of Department
EEE, VFSTRU, Vadlamudi

Coordinators

Mr. Ch. Saida Reddy
Asst. Professor, Cell:8790754670

Ms. K. Mercy Rosalina
Asst. Professor, Cell:8978856650

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Mr. M. Sandeep, Asst. Prof.

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A Five Day Faculty Workshop on "Power Electronics Applications to Emerging Trends in Energy Systems" June 27th - July 1st, 2017.



Organized by...

Department of
Electrical & Electronics Engineering



VIGNAN'S
Foundation for Science, Technology & Research
UNIVERSITY
[Bills Act 3 of UGC Act of 1956]
Accredited by NAAC with 'A' Grade

VADLAMUDI, Guntur Dist. - 522 213
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ABOUT UNIVERSITY

VFSTR University, the flagship institution of Vignan Group of Educational Institutions, is a NAAC 'A' accredited institution, ranked 88 by National Institutional Ranking Framework of MHRD, Government of India. Located in serene environs of Vadlamudi on the Guntur-Tenali highway, the university with its sprawling play grounds, campus greenery and imposing academic blocks, is a virtual haven of rural quiet and idyllic beauty. Since its inception in 1997, the institution has been striving to promote high standards in technical education to aid in the career building of many students who step into its portals. Through diverse programs and updated curriculum by imparting industry exposure and hands-on skills, the university trains its students into competitive and global professionals, imbued with ethical consciousness and social awareness. All the departments are supported by a good mix of young and senior faculty with a rich research, teaching and industry background. The sophisticated laboratories and research centres make it one of the most preferred institutions for the aspirants of engineering studies.

ABOUT THE DEPARTMENT

The Department of Electrical and Electronics Engineering was established in the year 1999 with the motto: *Excellence in Engineering Education*. The department strives for excellence in teaching, research and consultancy services to the industry. It has eminence in selected niche areas of Electrical and Electronics Engineering. The Department has highly qualified and experienced faculty with a strong research and professional expertise. Besides teaching undergraduate and post-graduate courses, the faculty members are engaged in a broad range of research and development areas in collaboration with renowned overseas universities, research institutions and multinational companies. The aim is to produce engineers who are flexible across disciplines and able to apply their knowledge and skills to solve complex problems. The department aims to produce thinking engineers, who will create new technologies for the future. The Department of Electrical and Electronics Engineering currently offers B.Tech in EEE, M.Tech programme in Power Electronics & Drives and Power Systems besides the Ph.D programme.

ROUTE MAP



OBJECTIVE OF THE WORKSHOP

Developing countries like India offers numerous opportunities and challenges to provide Energy to all its needs. Challenges related to harnessing the non-conventional energy, its storage and transmission is a research area in vogue. Recent advances in Power Electronics offers solutions to these challenges. Most of the renewable energy sources (RES) require advance power electronics converters for maximizing the production and enhancing the reliability during the operation. Highly efficient power converters at the renewable generation end can contribute to energy production maximization.

The goal of this workshop is to enhance the knowledge and skill of faculty, students and industry professionals and to explore new research areas and solutions to quickly develop, iterate and test control strategies in powerful hardware and comprehensive software tools for real-time applications.

WORKSHOP COVERS

- Introduction to Switch mode power converters
- Advanced SMPS based converters in Electrical Appliances.
- Recent Advances in Design of Power Electronic Converters.
- Recent Advances in Fabrication of Power Electronic Converters.
- Field visit.
- Effect of Electromagnetic Interference on Design of Electrical Appliances.
- New Approaches and Designs for Electromagnetic Interference and Electromagnetic compatibility.
- Thermal Management of Electronic systems
- Emerging research areas in power electronics

RESOURCE PERSONS

RESOURCE PERSONS :

Mr. V. Nagesh, CTO, Chirra Engineers Pvt. Ltd., Bangalore
Mr. M.V. Rayudu, CEO, Chirra Engineers Pvt. Ltd., Bangalore
Dr. G. Durga Sukumar, Principal, VITS, Hyderabad
Dr. G. Srinivasa Rao, HOD, EEE, VFSTR University, Vadlamudi
Mr. B. Murali, Technical Head, Honeywell, Hyderabad

ELIGIBILITY:

Faculty members of Engineering / Polytechnic Colleges approved by AICTE / Researchers / Industrial participants / PG students and Ph.D scholars are eligible.

REGISTRATION :

Faculty from Institutions : Rs. 1,500/-
Scientists & Industrial participants : Rs. 2,000/-
PG students & Ph.D.Scholars : Rs. 1,000/-

Participants can register by sending the registration form along with Demand Draft in favour of "VFSTR, General Funds" payable at Vadlamudi and it should reach on or before 23rd June, 2017.

To
Dr. G. Srinivasa Rao
Professor & HOD
Department of EEE
VFSTR University,
Vadlamudi – 522 213
Guntur (Dist), Andhra Pradesh

*Spot Registrations are available

- The registration fee includes Registration Kit, working lunch, tea and snacks during workshop.
- Accommodation will be provided on payment basis subject to availability.